

11:00 AM	Welcome & Opening Remarks <i>Mr. Tom McDermott</i> Chief Technology Officer, SERC & AIRC; Stevens Institute of Technology <i>Ms. Kara Pepe</i> Director of Operations, SERC & AIRC; Stevens Institute of Technology
11:10 AM	Session 1 Digital Engineering Impact on Design Cycle Time with the Lyneis Rework Cycle <i>Ms. Stephanie S. Chiesi</i> SERC Doctoral Fellow; Stevens Institute of Technology
11:40 AM	Borrowing from Systems Engineering: A Framework for Requirement Flow Down in Lattice Design <i>Mr. Joseph W. Fisher</i> Pennsylvania State University
12:10 PM	Break & Poster Expo
12:40 PM	Session 2 Study of Equivalence in Systems Engineering within the Frame of Verification and Systems Theory <i>Mr. Paul Wach</i> Virginia Tech
1:10 PM	Physics-informed Machine Learning for System Intelligence <i>Mr. Cheolhei Lee</i> Virginia Tech
1:40 PM	Applications of Graph Theory for Reuse of Model Based Systems Engineering Design Data <i>Mr. Daniel Herrington</i> Naval Postgraduate School
2:10 PM	Robustness of Decentralized Decision-Making Architectures in Command and Control Systems <i>Mr. Lewis N. Boss</i> George Washington University
2:40 PM	Break & Poster Expo
2:55 PM	Session 3 Theory-Grounded Guidelines for Solver-Aware System Architecting (SASA) <i>Mr. Athul Chakkithara Dharmarajan</i> Purdue University
3:25 PM	Software Systems Engineering for the Development, Acquisition, and Exploitation of Artificial Intelligence & Machine Learning-Based Systems at the Tactical Edge <i>Mr. Matthew Sheehan</i> Naval Postgraduate School
3:55 PM	Closing Remarks <i>Mr. Tom McDermott</i> Chief Technology Officer, SERC & AIRC; Stevens Institute of Technology <i>Ms. Kara Pepe</i> Director of Operations, SERC & AIRC; Stevens Institute of Technology
4:00 PM	Adjourn

